

Figure 1

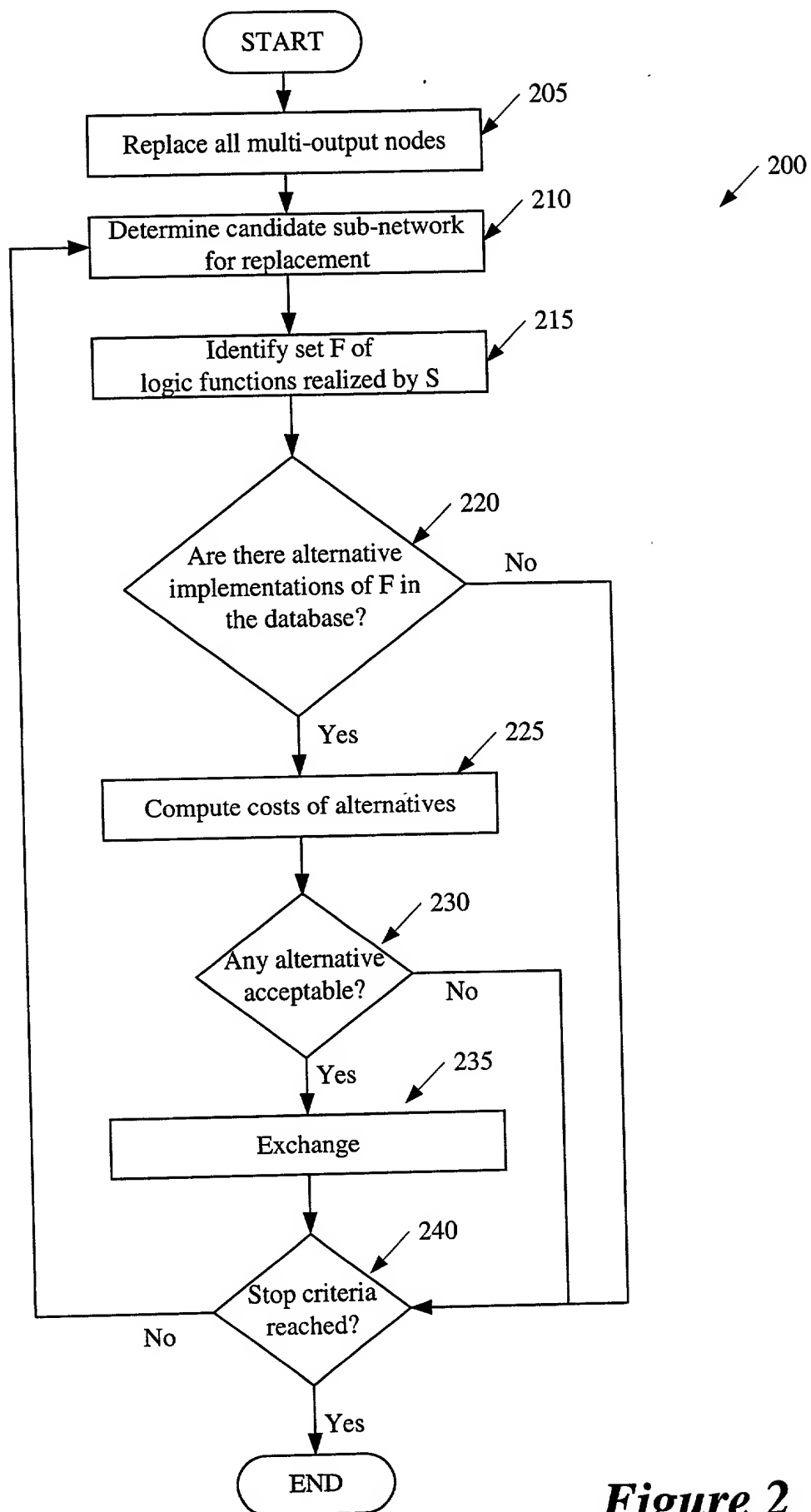


Figure 2

FIG. 3 is a schematic diagram of a logic circuit 300. The circuit 300 includes a first input Y0, a second input Y1, a third input Y2, a fourth input Y3, and a fifth input Y4. The circuit 300 also includes a first output X0, a second output X1, a third output X2, a fourth output X3, a fifth output X4, a sixth output X5, and a seventh output X6. The circuit 300 includes a first AND gate 305, a second AND gate 310, a third AND gate 315, a fourth AND gate 320, a fifth AND gate 325, a sixth AND gate 330, and a seventh AND gate 335. The circuit 300 also includes a first OR gate 300, a second OR gate 310, a third OR gate 315, a fourth OR gate 320, a fifth OR gate 325, a sixth OR gate 330, and a seventh OR gate 335.

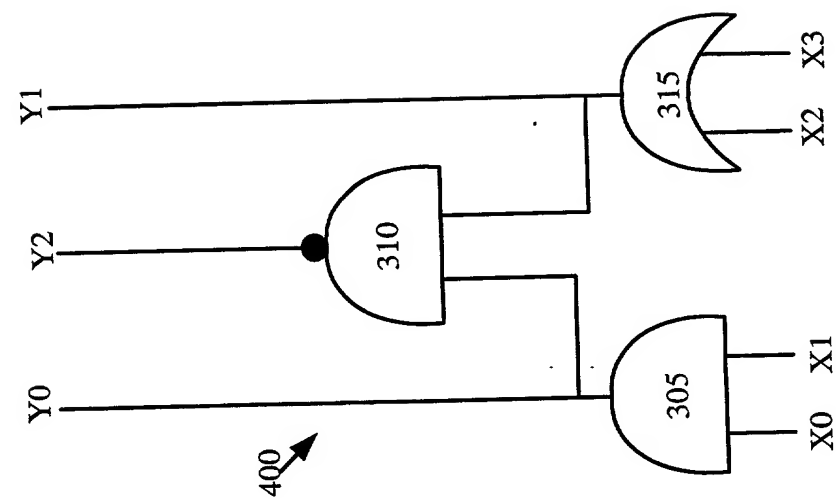


Figure 4

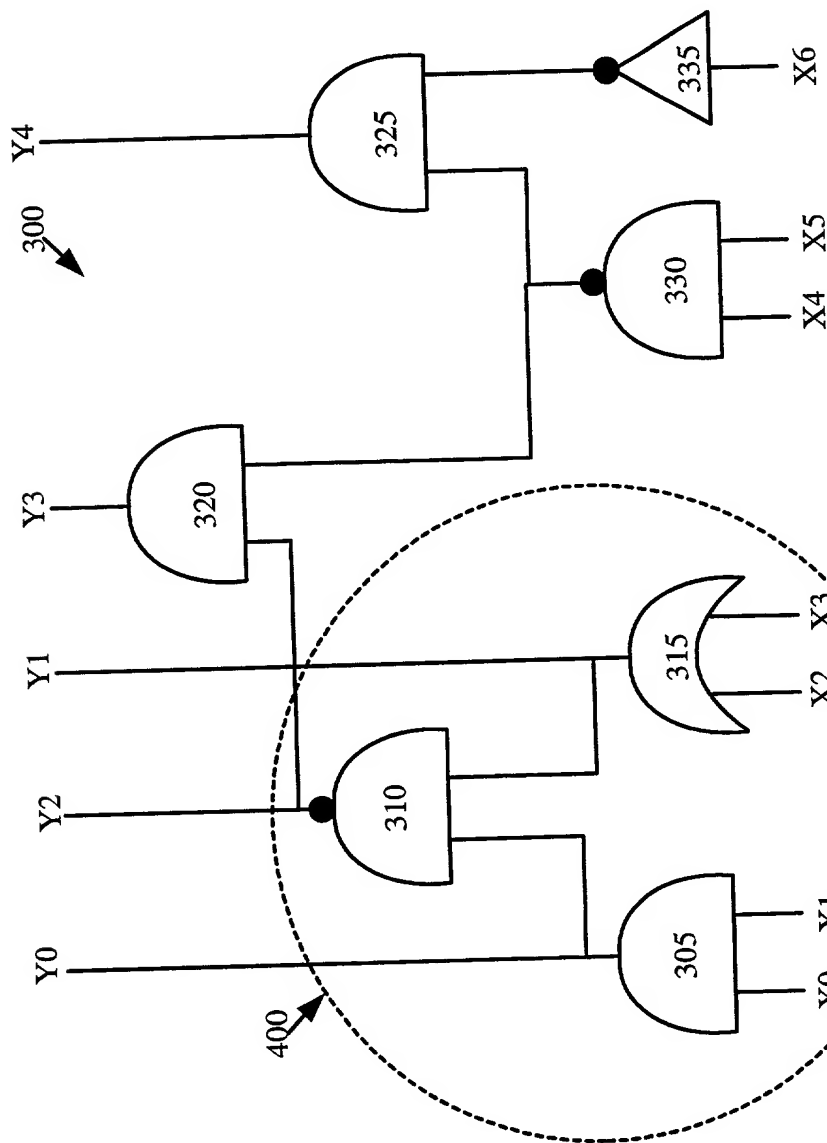


Figure 3

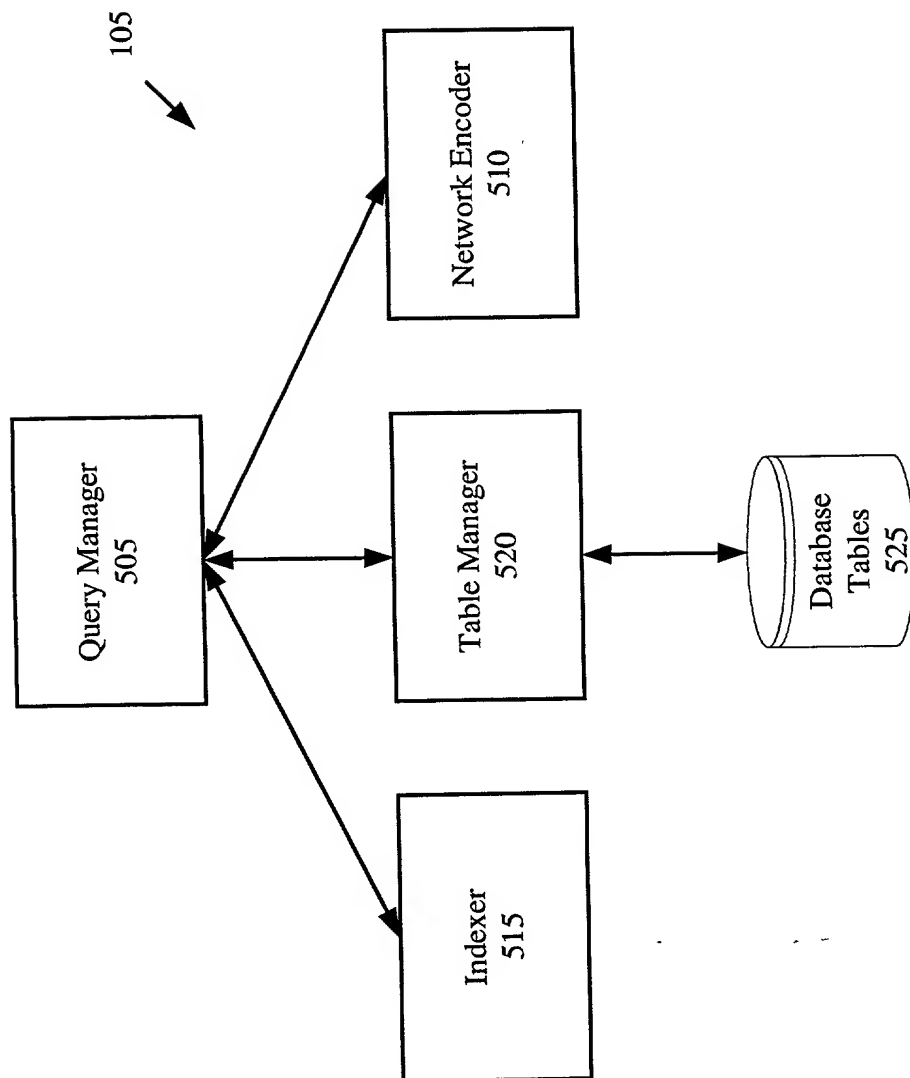


Figure 5

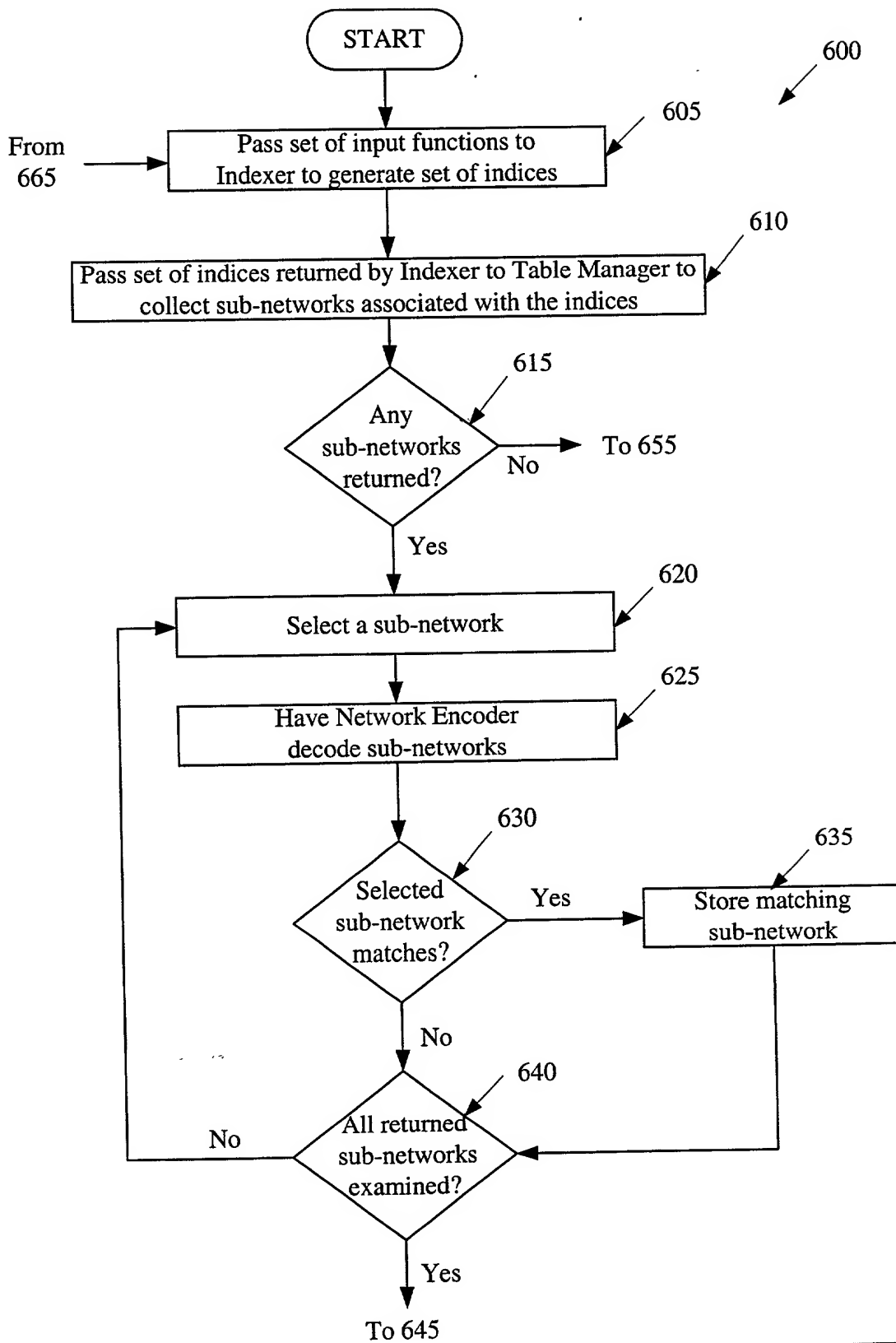


Figure 6A

Figure 6: Figure 6A
Figure 6B

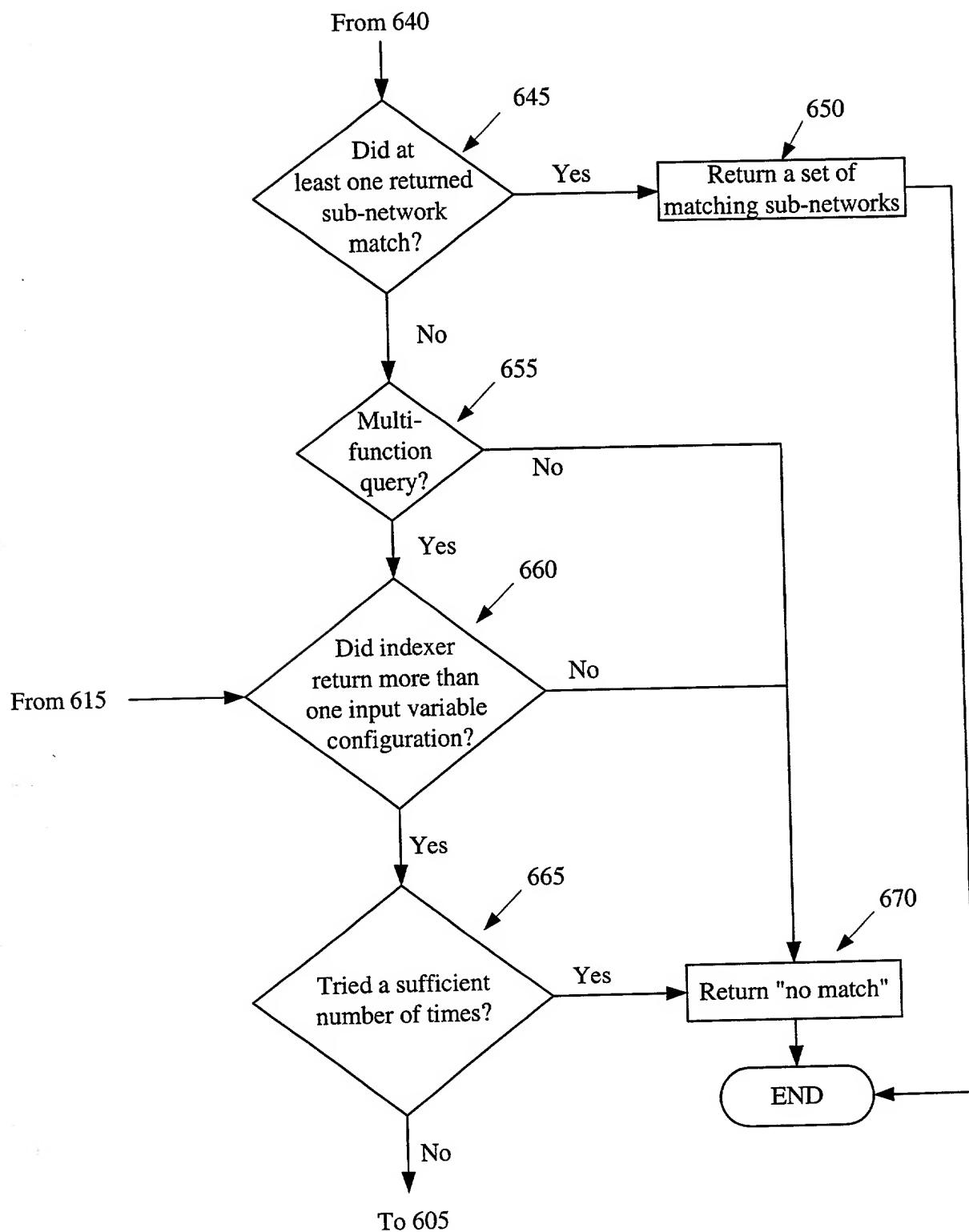


Figure 6B

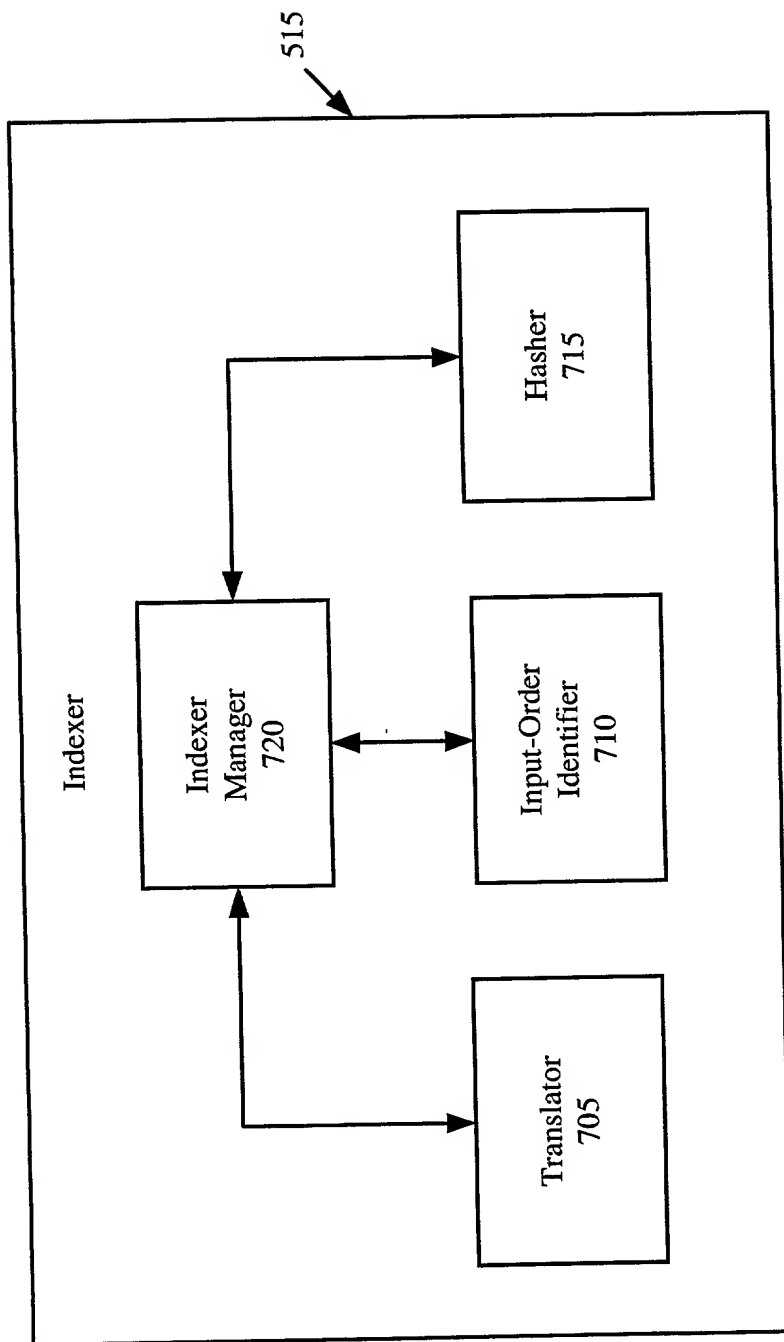


Figure 7

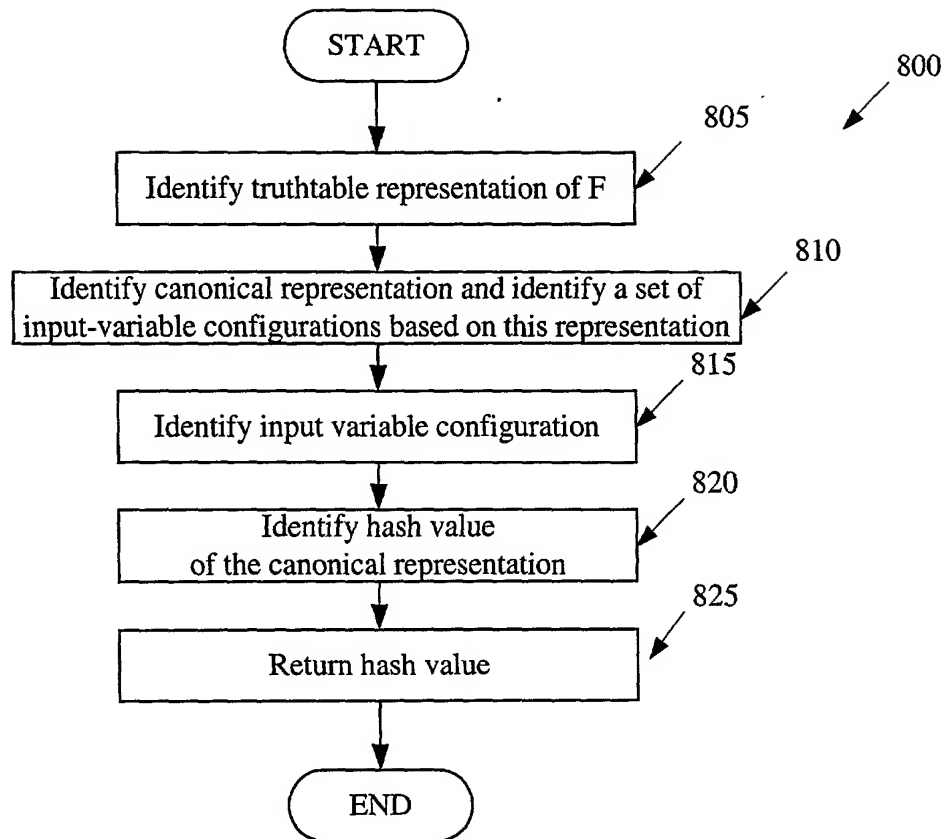


Figure 8

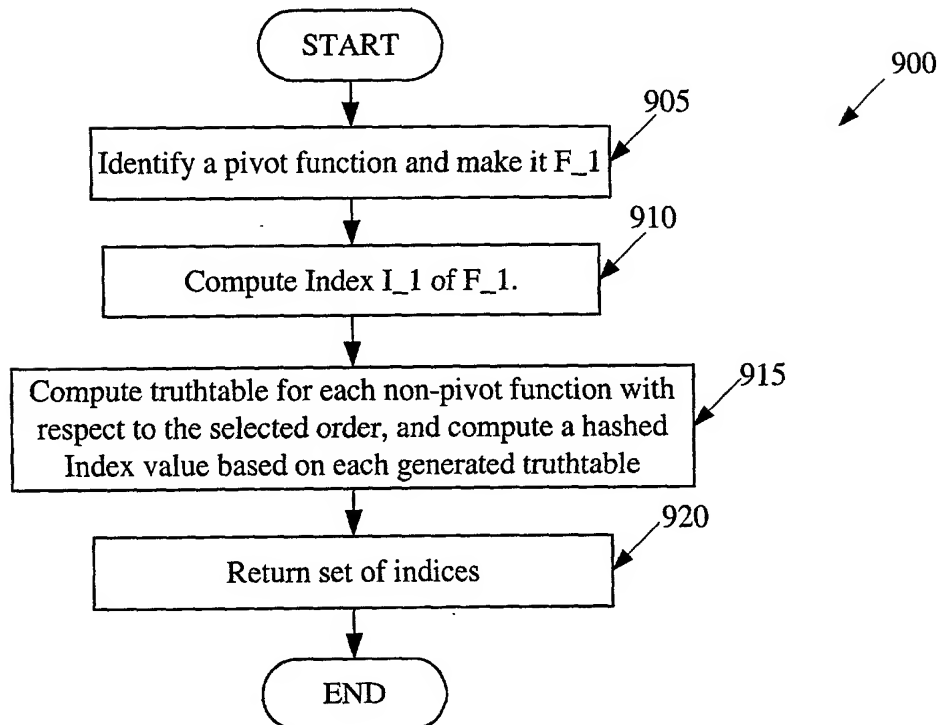


Figure 9

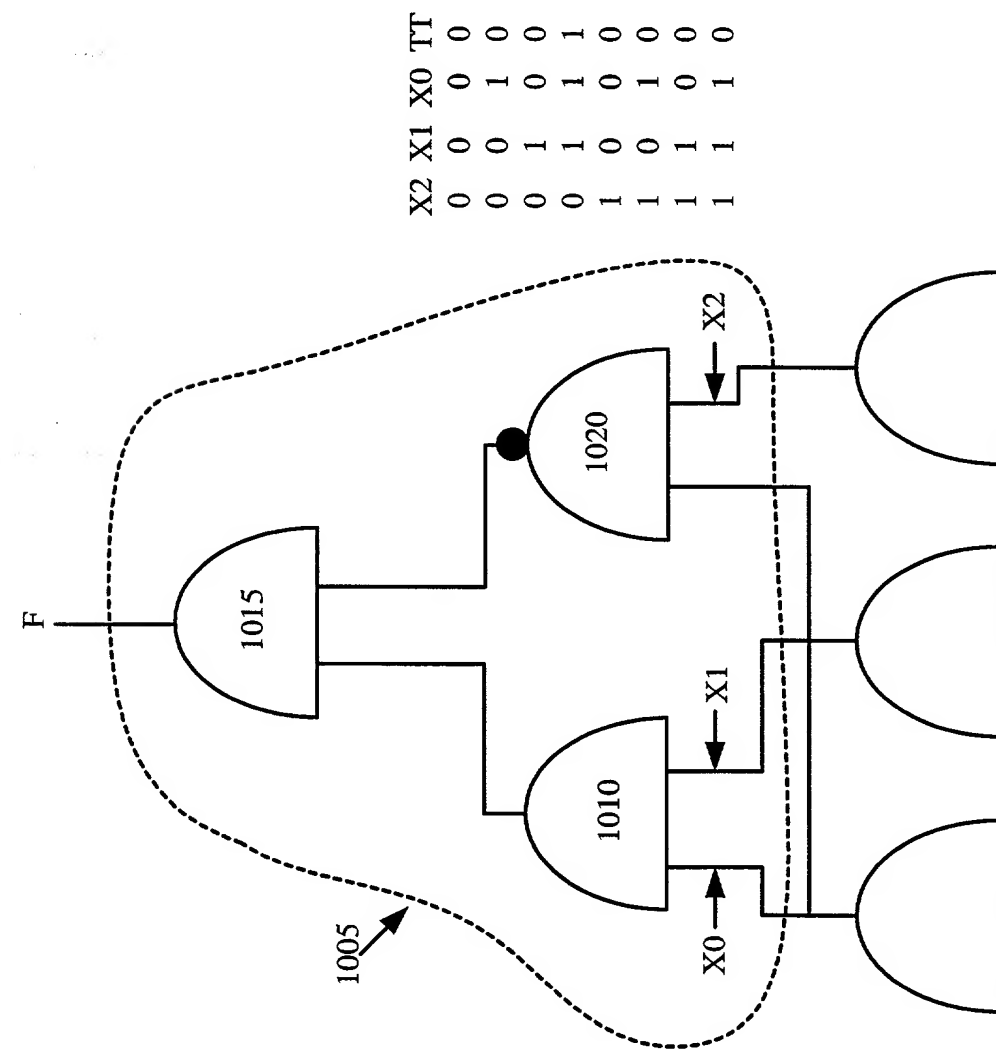


Figure 10

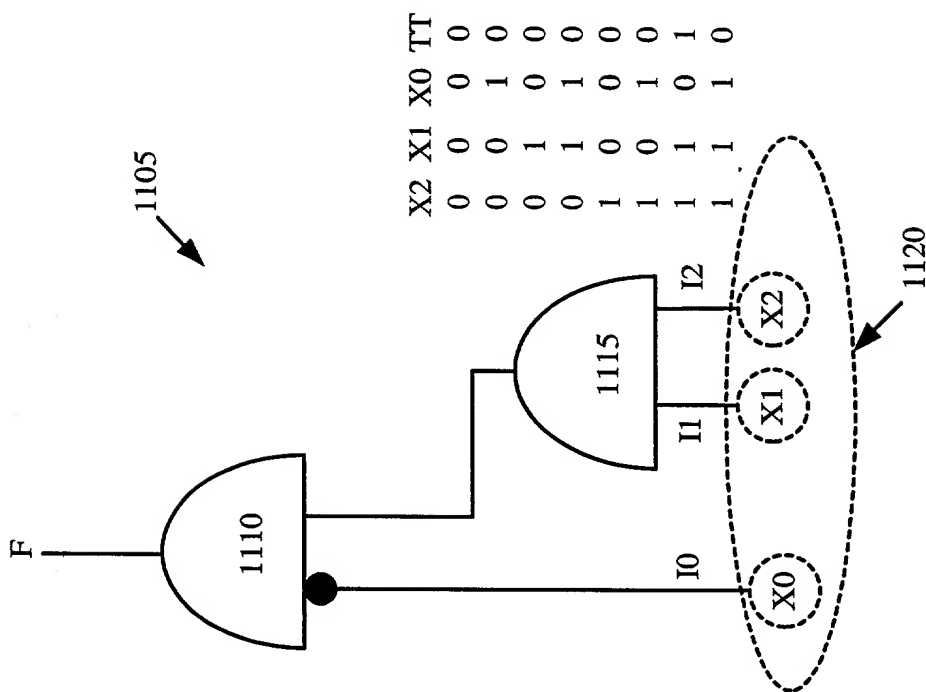
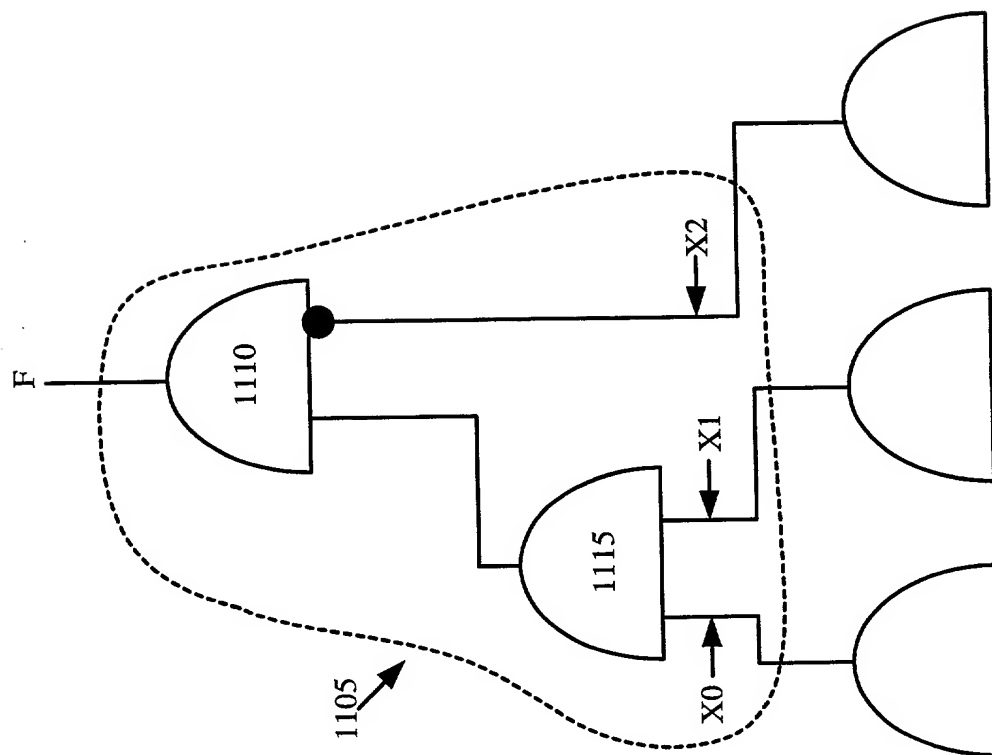
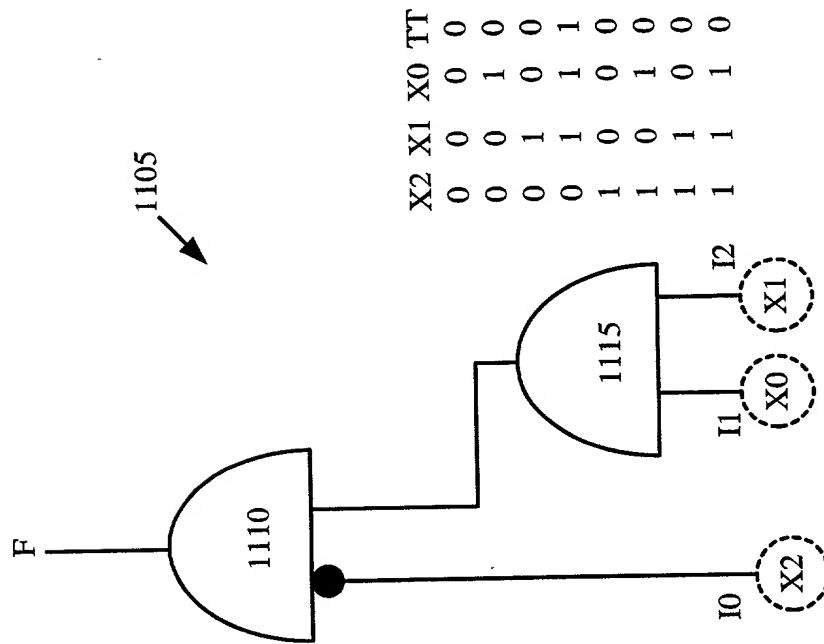


Figure 11



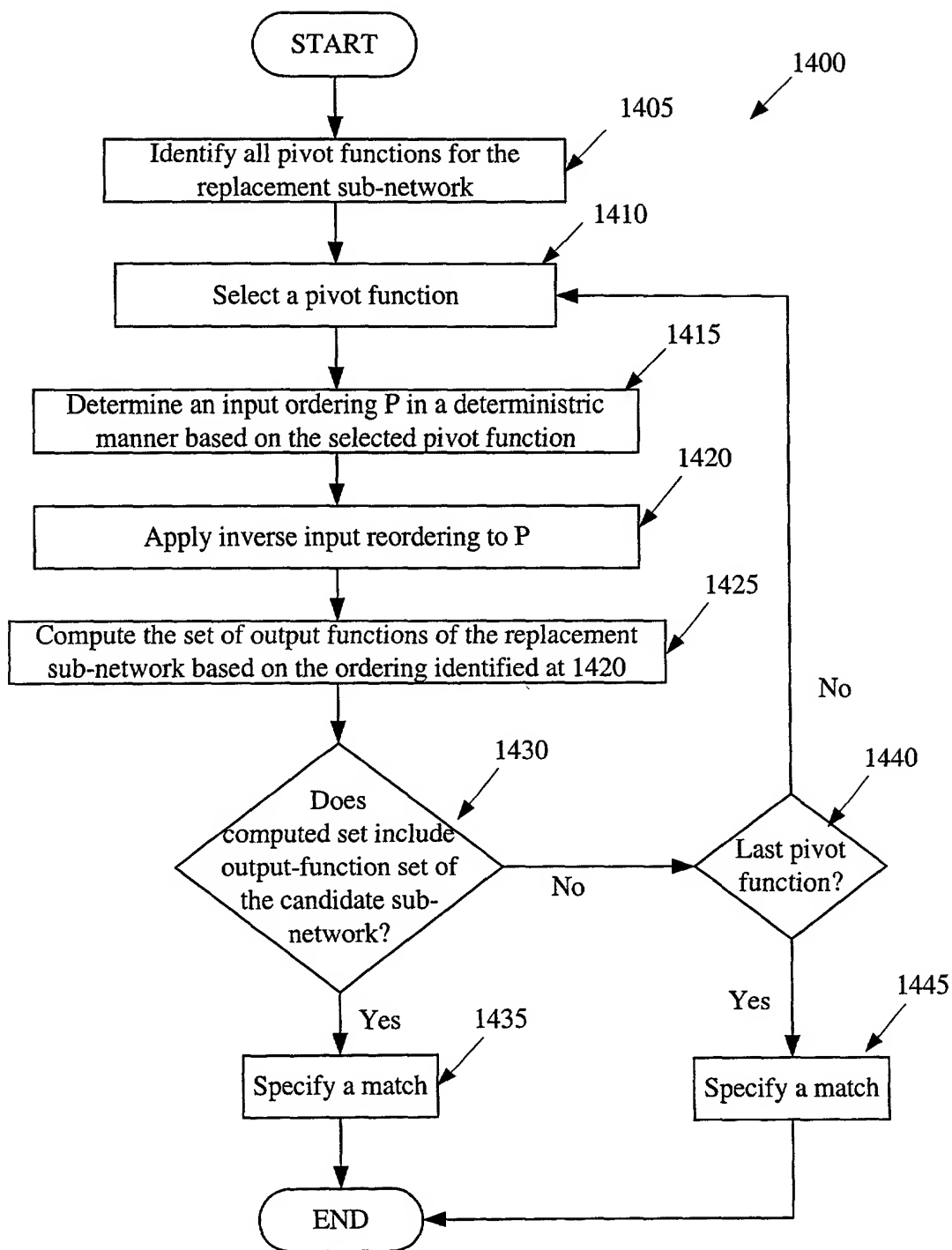


Figure 14

Pivot Index	Secondary Index	Network Indices	Row Length
Pivot Index #1	Index #T	N#U, ..., N#V	Length A
•	•	•	•
•	•	•	•
•	•	•	•
Pivot Index #1	Index #J	N#D, ..., N#E	Length B
•	•	•	•
•	•	•	•
•	•	•	•
Pivot Index #X	Index #H	N#F, ..., N#G	Length C

Pivot Index	First Row	Last Row
Pivot Index #1	Row #P	Row #Q
Pivot Index #2	Row #R	Row #S
•	•	•
•	•	•
•	•	•
Pivot Index #X	Row #Y	Row #Z

Figure 15

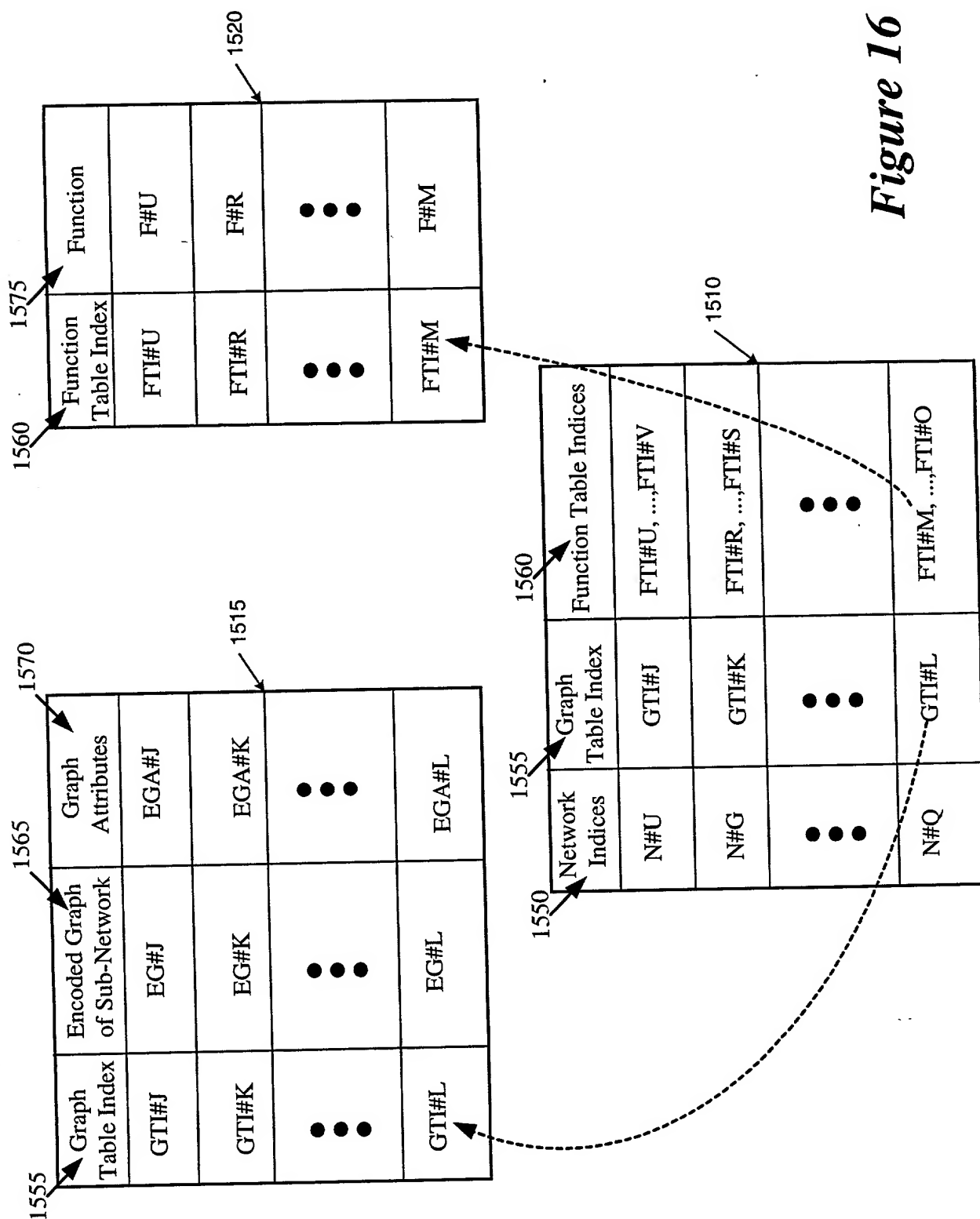


Figure 16

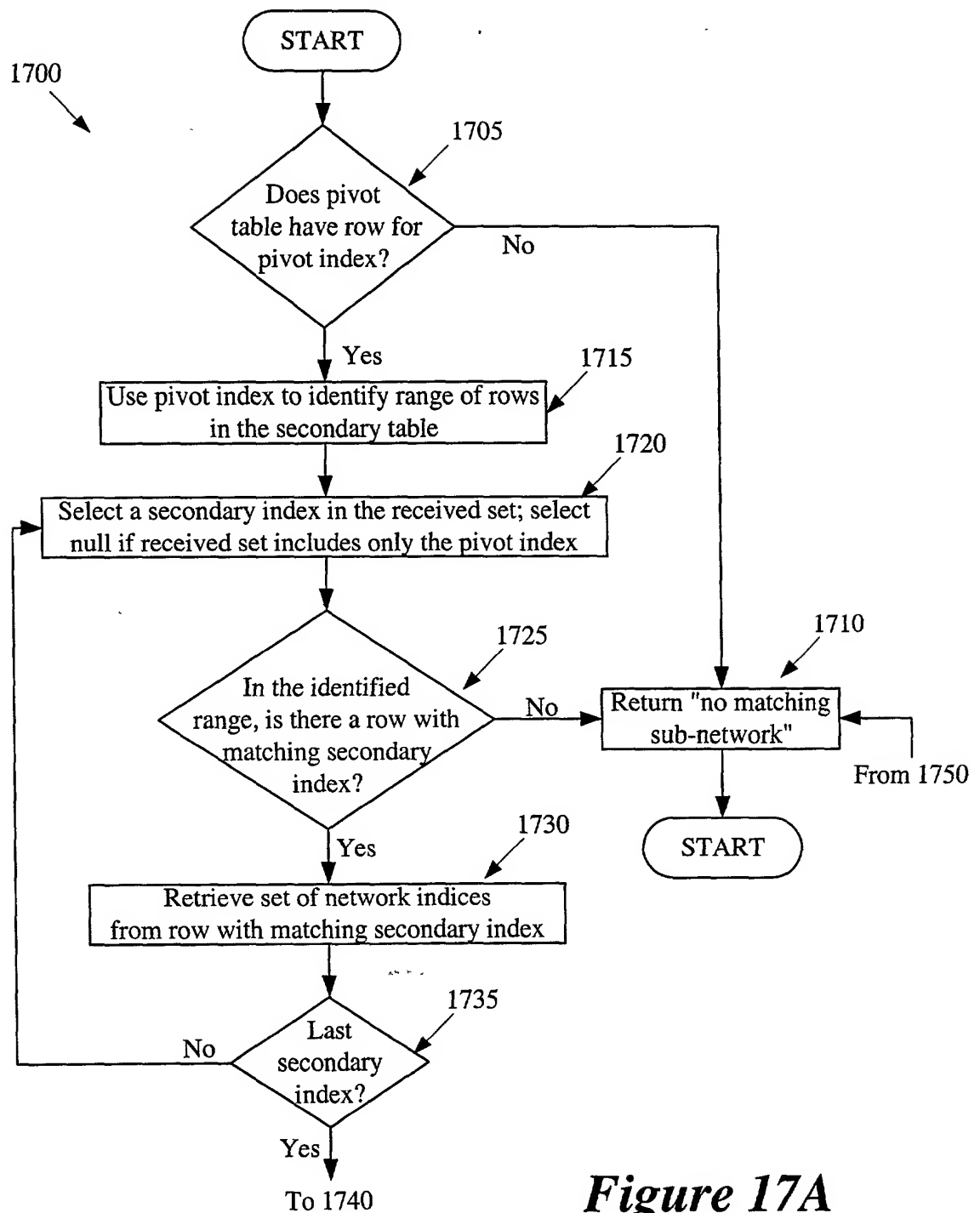


Figure 17A

Figure 17: Figure 17A
Figure 17B

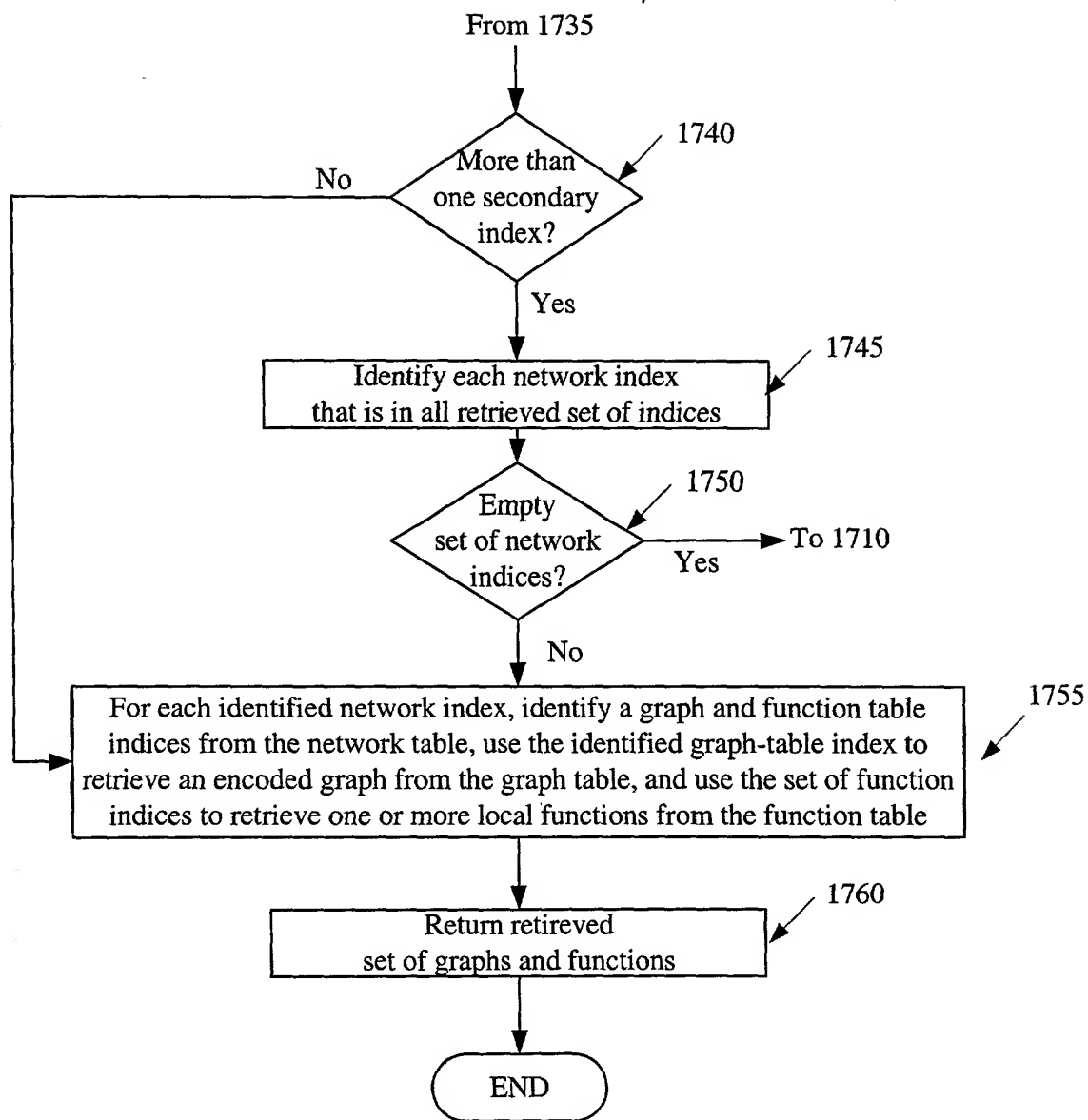


Figure 17B

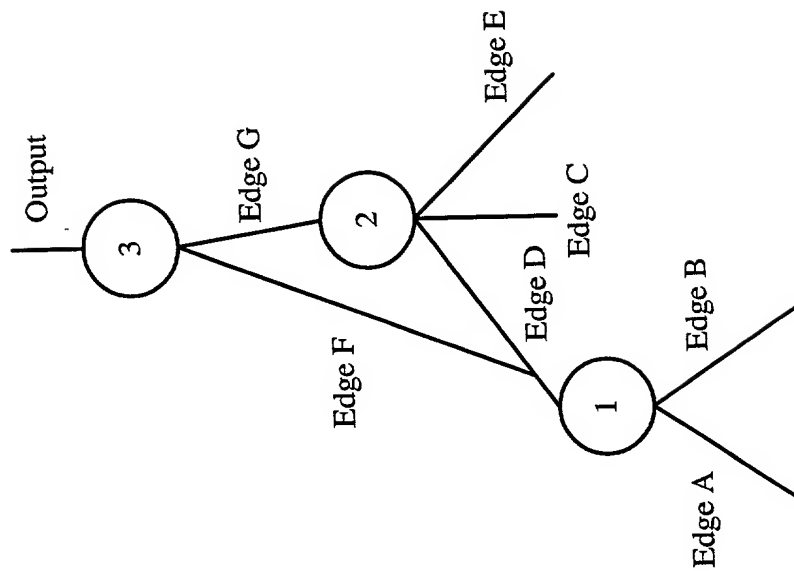


Figure 18

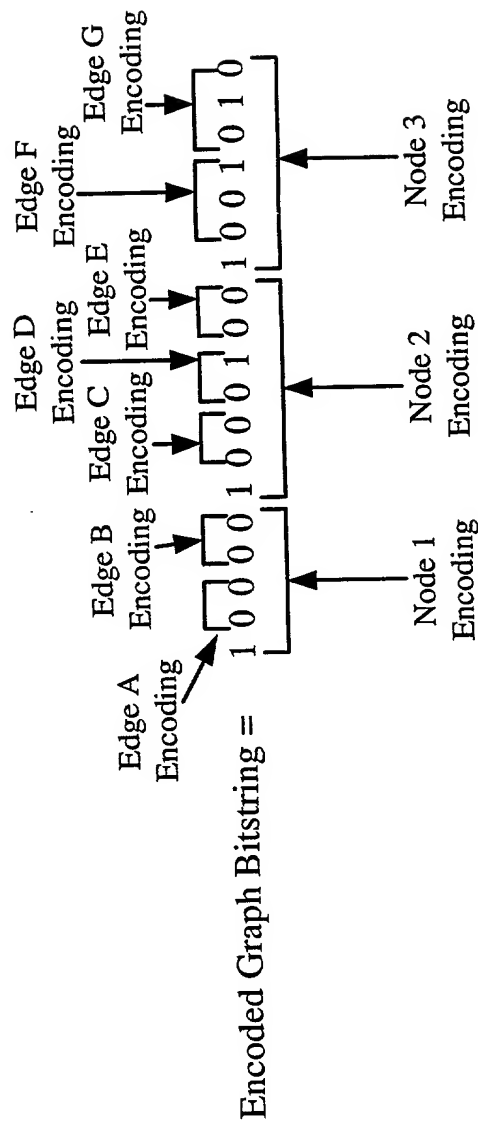


Figure 19

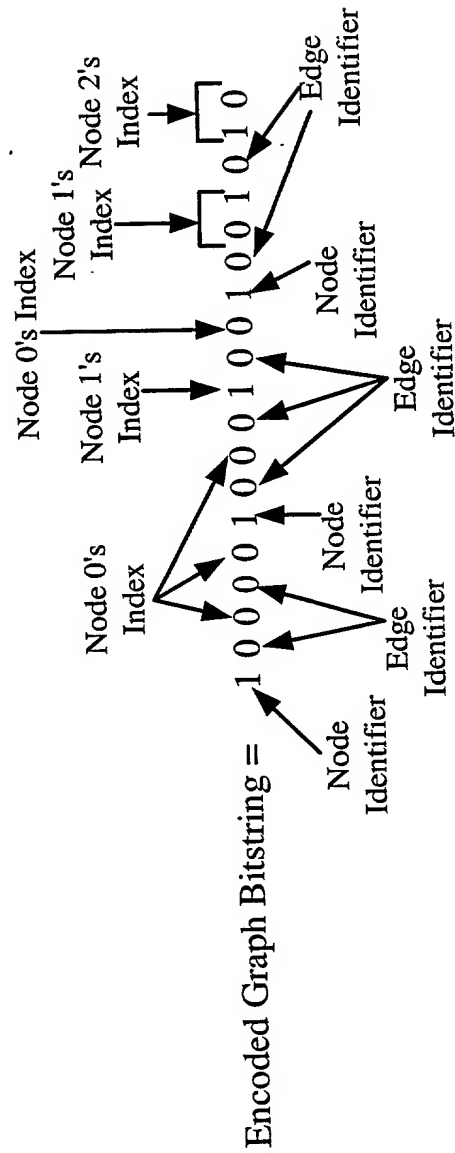


Figure 20

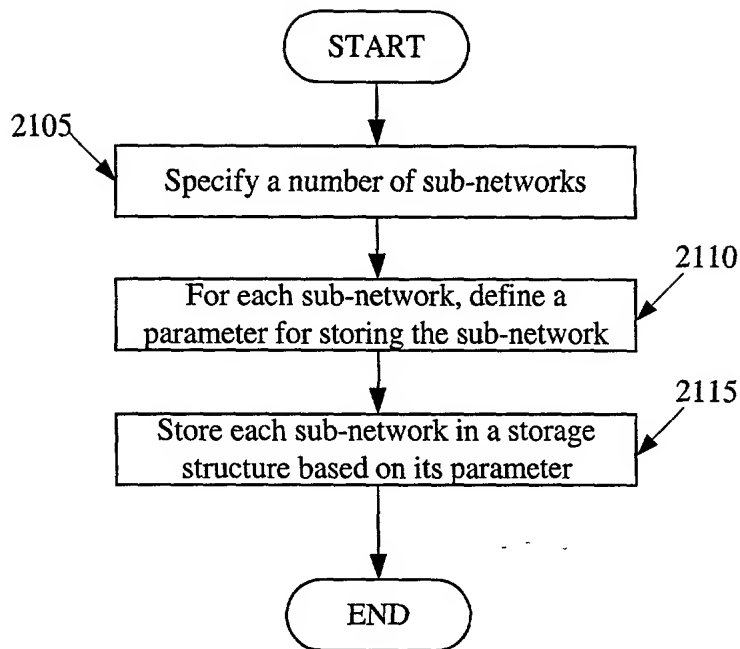


Figure 21

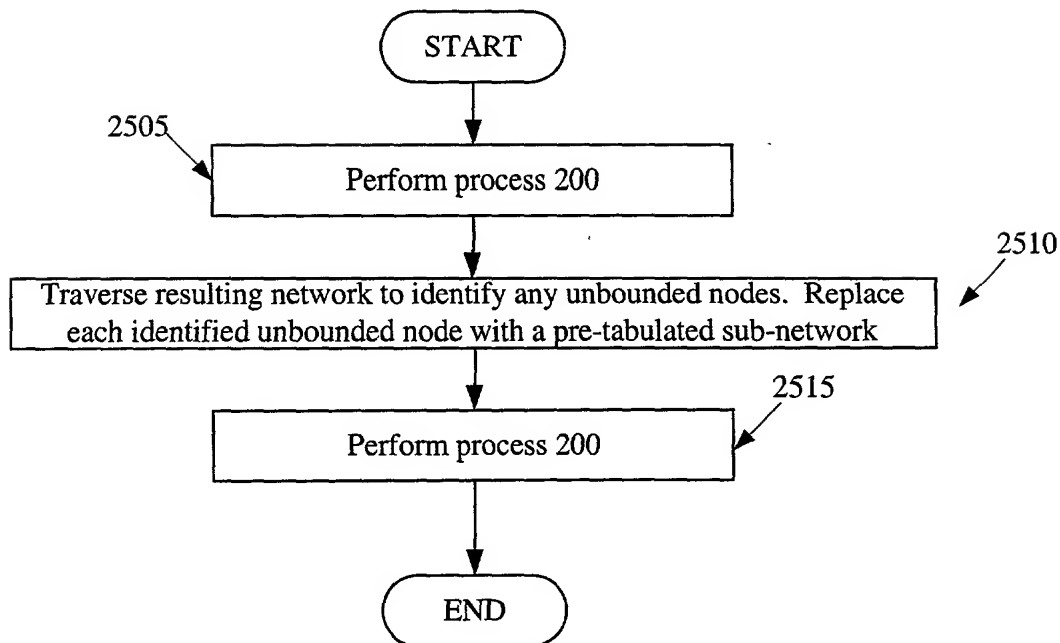


Figure 25

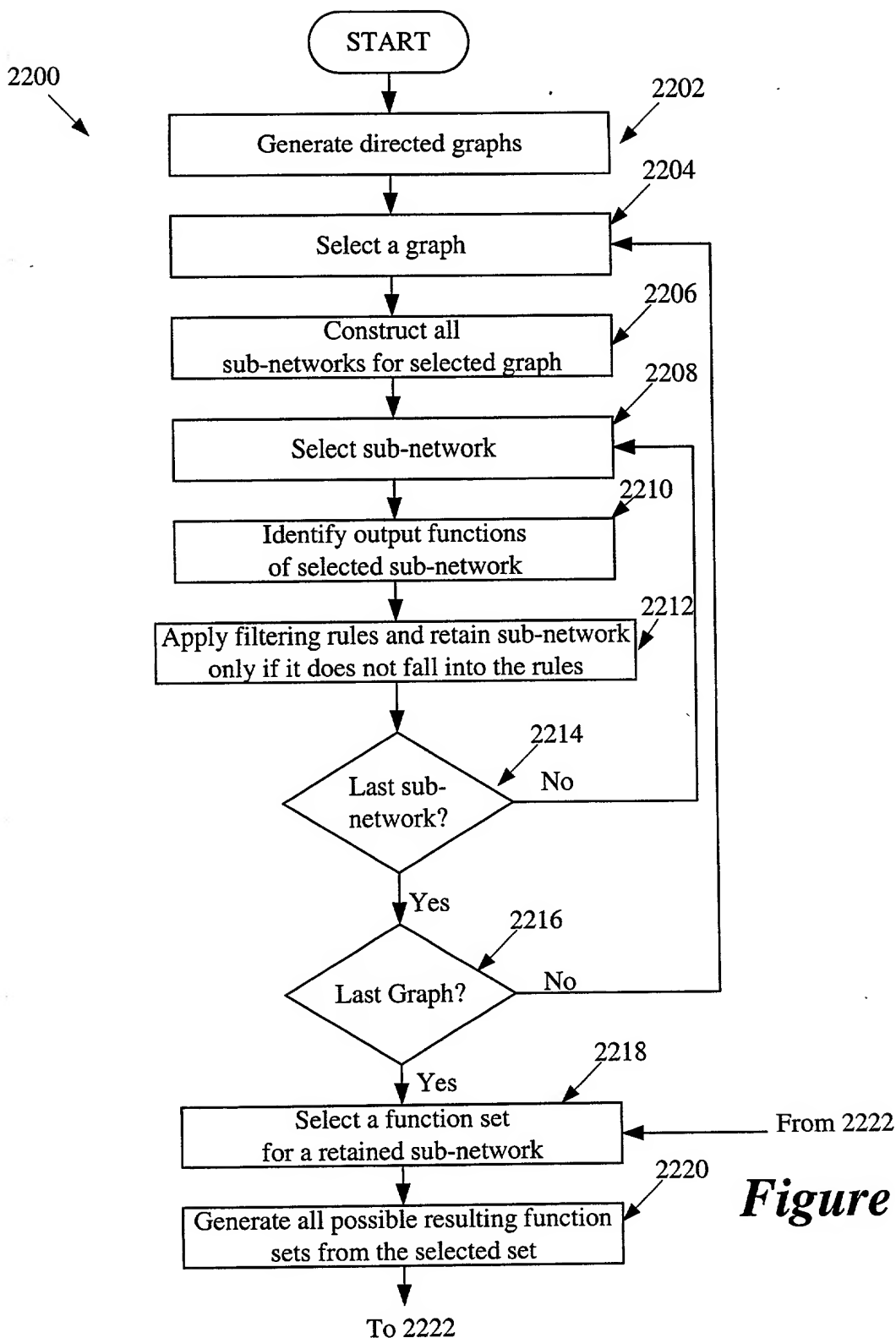


Figure 22A

Figure 22: Figure 22A
Figure 22B
Figure 22C

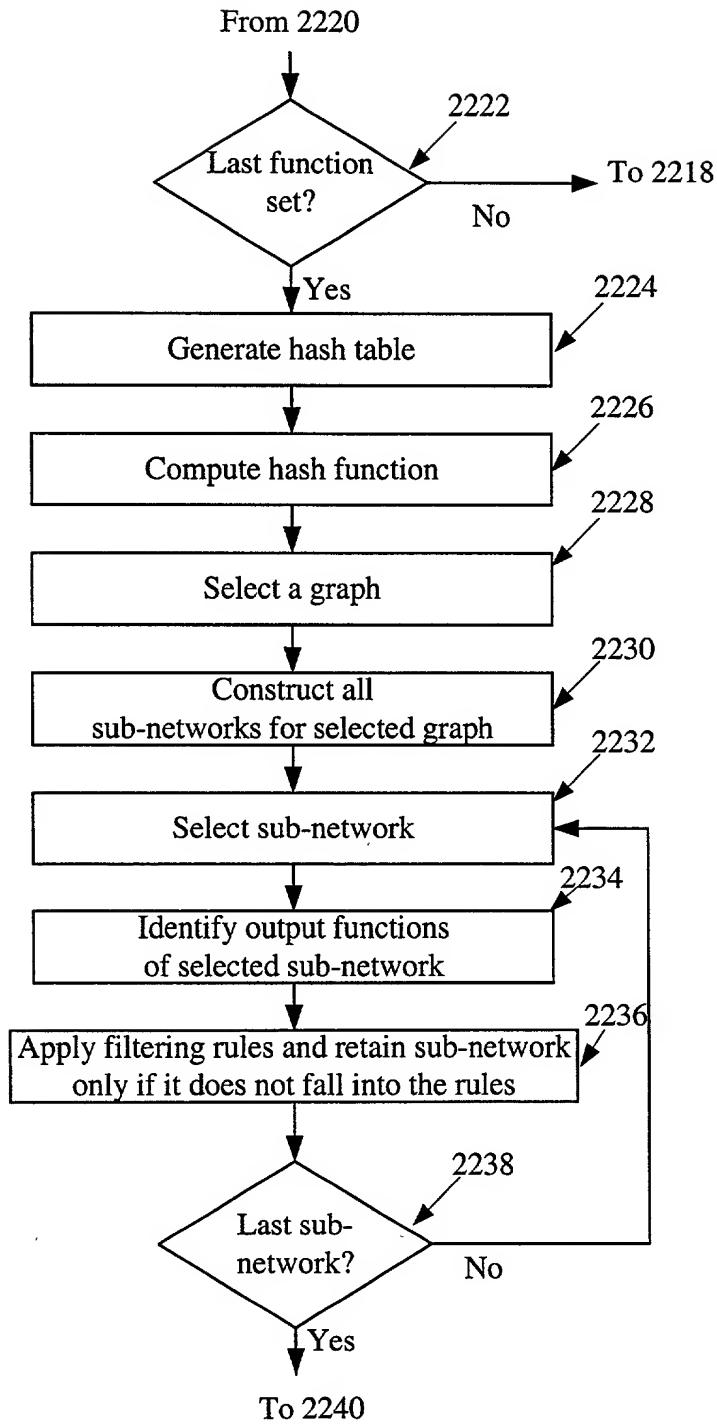


Figure 22B

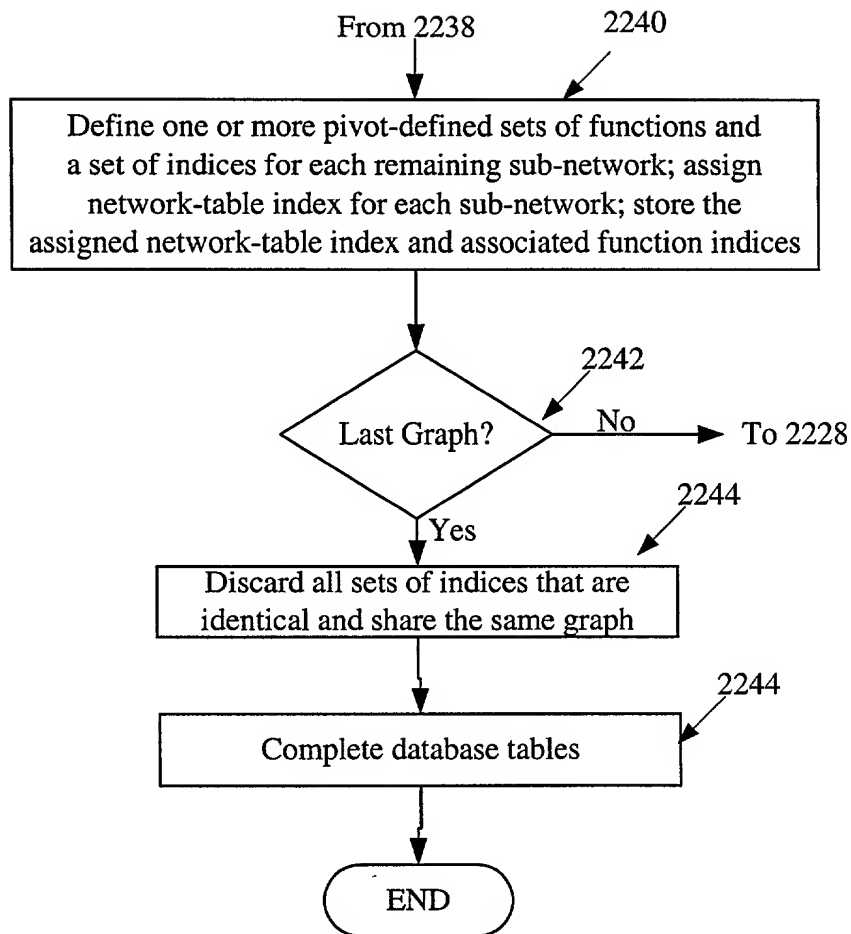


Figure 22C

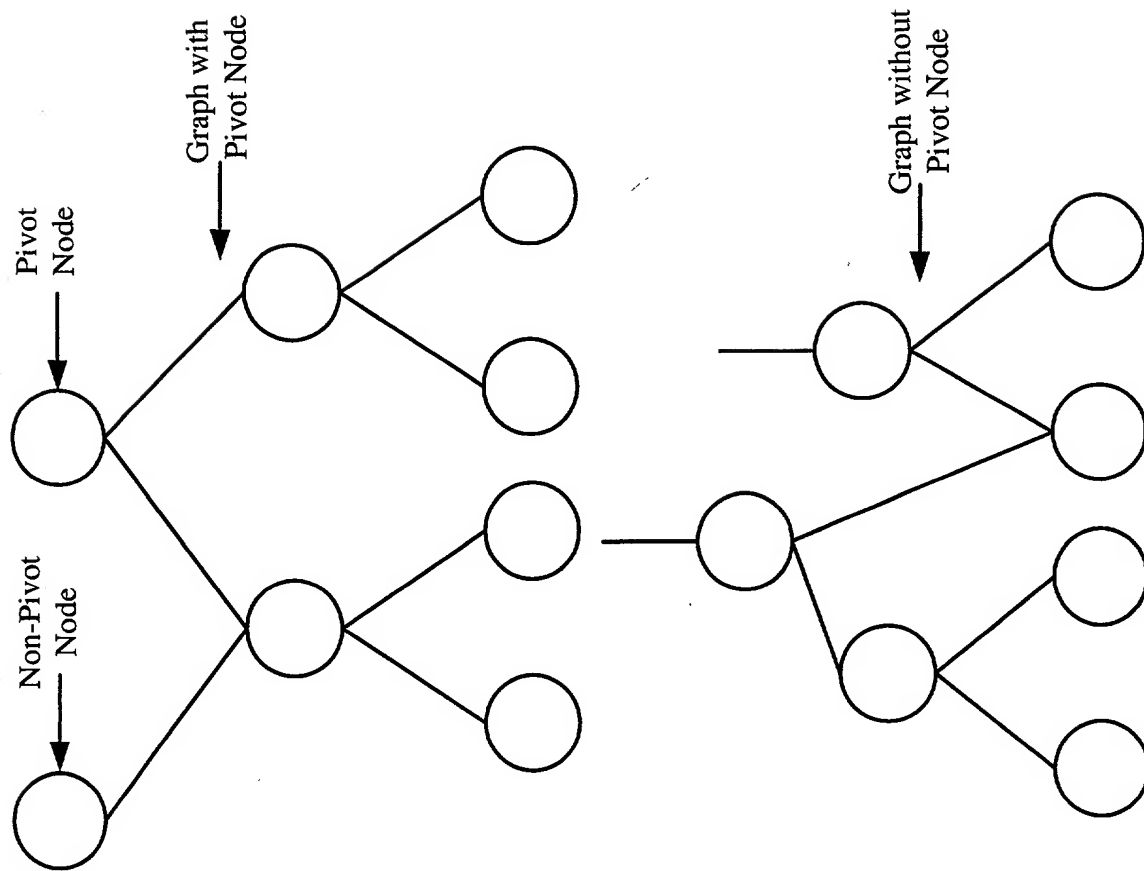


Figure 23

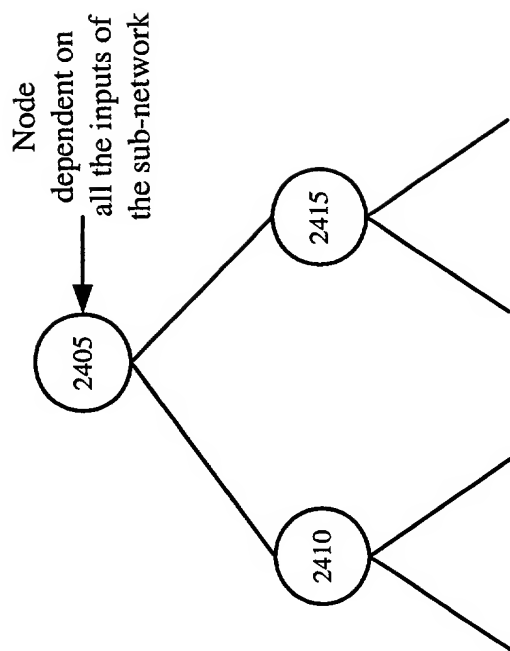


Figure 24

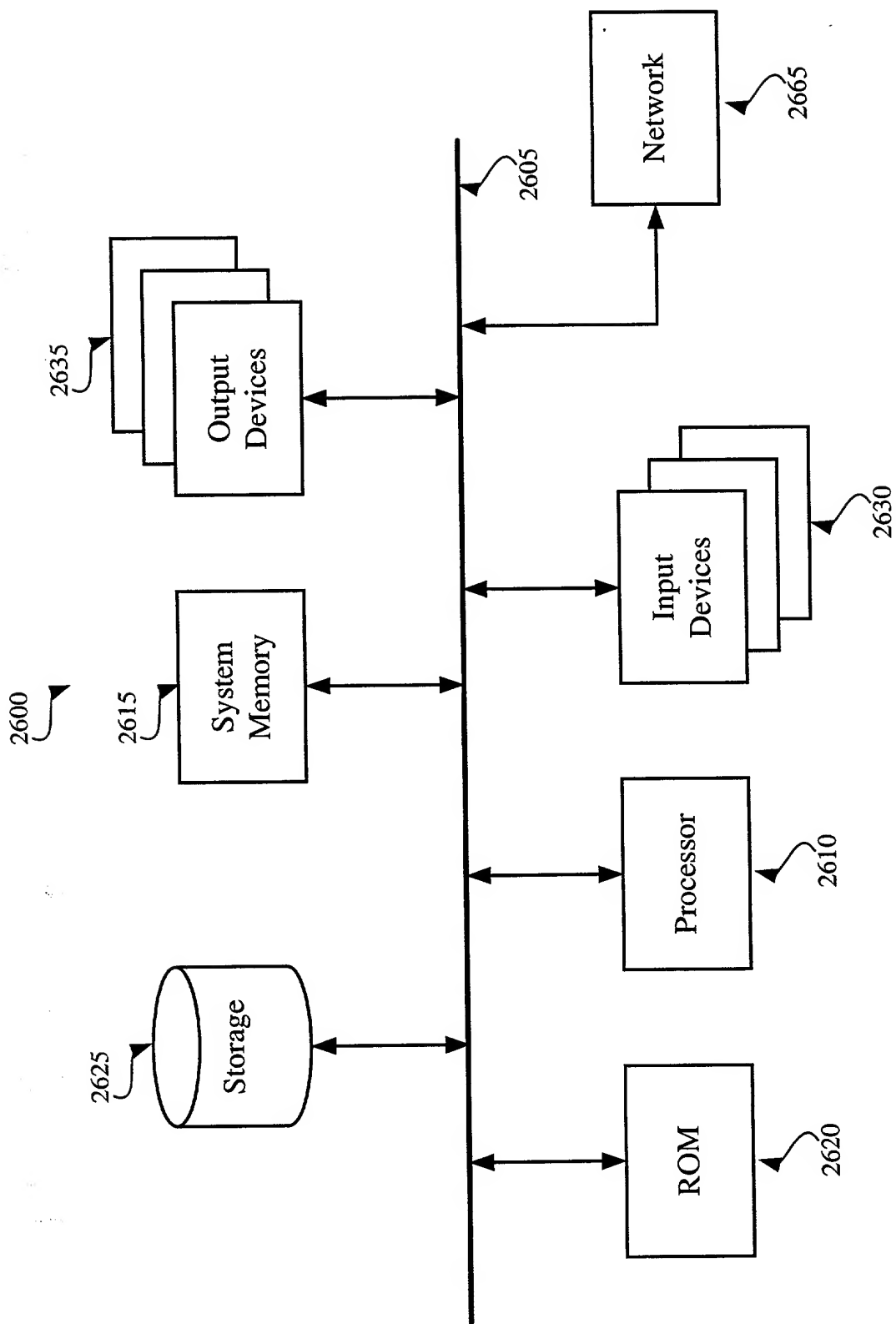


Figure 26